U.S. **UTILITY** Patent Application

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APPLICATION NO. 09/881005 F 257 SUBCLASS SUBCLASS ART UNIT EXAMINER SIDENCE CONTINUE CONTINUE

Michio Horiuchi Takashi Kurihara Shigeru Mizuno

Multi-layered semiconductor device and method for producing the same

PTO-2040 12/99

ISSUING CLASSIFICATION

ORIGINAL CROSS REFERENCE(S)

CLASS SUBCLASS CLASS SUBCLASS (ONE SUBCLASS PER BLOCK)

INTERNATIONAL CLASSIFICATION

Continued on Issue Slip Inside File Jacket

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| TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| The term of this patent | | | NOTICE OF ALLOWANCE MAILED | | |
| subsequent to (date) has been disclaimed. | (Assistant Examiner) (Date) | | · · | p. | |
| The term of this patent shall not extend beyond the expiration date | | | The state of the s | | |
| of U.S Patent. No. | | | ` | ISSUE FEE | |
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| this patent have been disclaimed. | (Legal Instrume | nts Examiner) | (Date) | | |
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| orm PTO-438A Rev. 6/99) | | | FILED WITH: | DISK (CRF) | FICHE CD-ROM |

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